

SWITCHING OVER TO A 64-BIT JRE

In order to fully utilize the 64-bit architecture on Windows[®], you will need to switch over to the 64-bit Java Runtime Environment (JRE).

► **To switch Scheduler to run with a 64-bit JRE:**

- 1 Uninstall any 32-bit JRE.
- 2 Install the 64-bit JRE.

Tidal recommends the SUN 64-bit 5.0 Update 11 JRE for AMD 64 and Intel EM64T processors. You can find the download at:

http://java.sun.com/javase/downloads/index_jdk5.jsp

Tidal recommends the BEA JRocket 5.0 64-bit JVM for Intel Itanium processors. You can find the download at:

http://commerce.bea.com/products/weblogicrocket/5.0/jr_50.jsp



Note You will need to install the 64-bit SAP JCO to connect to SAP environments.

- 3 Go through a normal master installation.



Note The install may warn that no supported JVM is detected. You can ignore this warning.

- 4 Copy the *samaster64.exe* file into the ... \Tidal \Scheduler \master directory.



Note For Windows 2008, copy the *servicemgr.exe* file into the ... \Tidal directory.

- 5 Select **Start>Programs>Tidal Software>Tidal Service Manager** to open the **Tidal Service Manager** dialog.

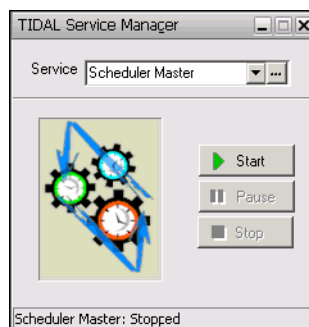


Figure 1 Tidal Service Manger Dialog

Tech Note

- 6 Click the ellipsis **...** button in the top right corner of the dialog.
The **Service Configuration** dialog displays.

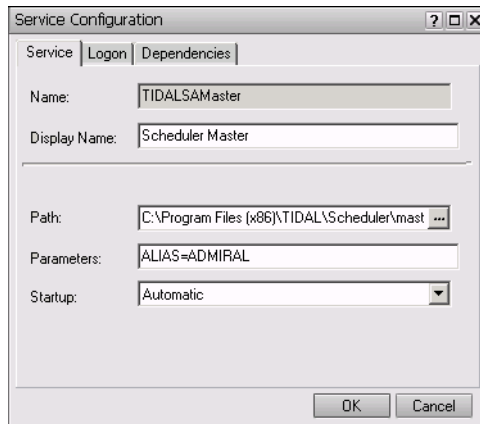


Figure 2 Service Configuration Dialog

- 7 On the **Service** tab, modify the **Path** field to point from *samaster.exe* to *samaster64.exe*, and click **OK**.
- 8 Start the Master service.
- 9 Apply the above steps to the Primary Master, Backup Master and Fault Monitor using these files:
 - ❖ *samaster64.exe* – Primary and Backup master machines running on an AMD 64-bit processor (or Intel EM64T capable processor)
 - ❖ *faultmon64.exe* – Fault Monitor machines running on an AMD 64-bit processor (or Intel EM64T capable processor)
 - ❖ *samasterIA64.exe* – Primary and Backup master machines running on an Intel Itanium processor
 - ❖ *faultmonIA64.exe* – Fault Monitor machines running on an Intel Itanium processor