



# **Release Notes for Cisco D9858 Advanced Receiver Transcoder for Software Version 4.01**

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## **Introduction**

This document describes the resolved caveats, open caveats, hash digest values, and upgrading the FPGA in software Version 4.01 for the Cisco D9858 Advanced Receiver Transcoder. This version supersedes Version 4.00, providing resolutions for several issues.

## **New Features**

There are no new features for the D9858 in software Version 4.01.



# Caveats

This section includes open and resolved caveats for this release. Links are provided to the Bug Toolkit where you can find details about each caveat.

This section includes the following topics:

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## Open Caveats - Version 4.01

The following is a list of open caveats in Version 4.01. The caveat ID links to the Cisco Bug Toolkit, where you can find details about the caveat.

- [CSCui91423](#)

When using Google Chrome Version 29.0 or higher, or Firefox Version 25.0 or higher, some functions of the web GUI are not supported properly.

## Resolved Caveats - Version 4.01

The following is a list of resolved caveats in Version 4.01. The caveat ID links to the Cisco Bug Toolkit, where you can find details about the caveat.

- [CSCui61278](#)

The units that decode the H.264 SD video and MPEG-1 Layer 2 audio are randomly rebooting.

- [CSCui77590](#)

The backup and restore fails when the MIB Object IDs do not match.

- [CSCuj95893](#)

There is a loss of audio on the AES output when the SBR is switched on or off.

- [CSCul47517](#)

The unit may freeze when displaying the **About > Features** menu on the front panel. The right/left arrow buttons trigger a reboot.

- [CSCul53315](#)

The service replacement is not operational when tuned to a system with unknown descriptor in the stream.

- [CSCul63142](#)

The unit may discard all settings after an uncontrolled power cycle.

- [CSCul70100](#)

The audio driver stops attempting to correct the lip sync value after receiving a high offset limit value.

- [CSCul90273](#)

The main processor resets due to an I2C failure.

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- [CSCuI94757](#)

When a DPI event is received, it causes Cue Trigger 1 to momentarily close.

- [CSCuM16637](#)

The Auto mode for MOIP output causes continuity count errors in the output stream.

- [CSCuI03168](#)

Due to a system time clock error, the video and audio pauses or skips every 5 to 10 minutes on the D9858 ASI transcode output.

- [CSCuI38465](#)

Due to the FPGA frame count mode change, the video and audio pauses or skips every 5 to 10 minutes on the D9858 ASI transcode output.

## Upgrading the D9858 FPGA

We recommend that you upgrade the D9858 FPGA version to R00.00.05 after you upgrade the D9858 to software Version 4.01. For more information on upgrading the software version, see the *Cisco D9858 Advanced Receiver Transcoder Software Version 4.0 Installation and Configuration Guide*.

**Note**

This upgrade can only be done through direct access to the unit.

- Step 1** From the user interface of the D9858, choose **System Settings > Versions**. The Versions page is displayed.

The screenshot displays the Cisco D9858 Advanced Receiver Transcoder web interface. The top navigation bar includes links for Summary, Input, Audio & Video, Transport Stream, System Settings, and Support. The left sidebar shows a menu with System, Alarms, Versions, Settings File, and Security/Accounts. The main content area is titled 'Versions' and contains several sections:

- Current Versions:** A table showing the current versions of the SW, FPGA, and TMB.
- Version Controls:** A section with dropdown menus to select specific versions for the SW and FPGA.
- Main Board - Firmware Versions:** A table listing the firmware versions for various components like Boot, CPLD, FP-PIC, and FPGA.
- HW Board Versions:** A table showing the hardware board details, including Board, Rev, Option Bits, and Serial Nr.
- APP / FPGA Downloads:** A section for uploading files, with a 'Browse...' button and a 'Download' button. It includes a note about automatic reboots.
- Download Status:** A section showing the status of downloads, with a 'History' button.

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- Step 2** In the **APP/FPGA Downloads** area, click **Browse**.
- Step 3** Choose the FPGA R00.00.05 CDT file and click **Open**.
- Step 4** Click **Download** to download the upgrade file. The **State** field in the **Download Status** area displays the progress of the download. When the download is complete, a Successfully Completed message is displayed at the top of the Versions page.
- Step 5** From the **FPGA** drop-down list, choose the new FPGA firmware Version R00.00.05. The Select Version dialog box is displayed.
- Step 6** Click **Select & Reboot**. The D9858 transcoder will reboot and use the new FPGA version.

## D9858 Hash Digest Values

You can generate the hash digest to verify the integrity of the software and FPGA versions running on the D9858 transcoder. For more information on generating the hash digest values, see the *Cisco D9858 Advanced Receiver Transcoder Software Version 4.0 Installation and Configuration Guide*.

[Table 1](#) lists the official D9858 v4.01 hash digest values.

**Table 1** *D9858 v4.01 Hash Digest Values*

Code/Processor	Hash Digest	Version
APP/5514	f63bab4f 63335657 c7159afa 998411fc fc831f6f	R04.01.00DD
APP/7109	98178c55 89f0c043 ffd988c0 b4ae064d 253868d4	R04.01.00DD
APP/SAT	16f42d0a 58e317dd ab35e4de a3cb8a0e f3154174	R04.01.00DD
FPGA FX60/7109	edd457b4 03ccaa84 84635a9e 1704999e 733616a4	R00.00.05az

If these values do not match the generated digest values, contact Cisco Services.

## Related Documentation

*Cisco D9858 Advanced Receiver Transcoder Software Version 4.0 Installation and Configuration Guide*  
*Open Source Used In D9858 Advanced Receiver Transcoder Software Version 4.0*

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# Obtaining Documentation and Submitting a Service Request

For information on obtaining documentation, using the Cisco Bug Search Tool (BST), submitting a service request, and gathering additional information, see *What's New in Cisco Product Documentation* at: <http://www.cisco.com/en/US/docs/general/whatsnew/whatsnew.html>.

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