

Located c1100-universalk9.17.03.05.SPA.bin

[illegible]

```
Package header rev 3 structure detected
IsoSize = 559636480
Performing Integrity Check ...
Performing Signature Verification ...
RSA Signed RELEASE Image Signature Verification Successful
```

Image validated

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Cisco Systems, Inc.
170 West Tasman Drive
San Jose, California 95134-1706

Cisco IOS Software [Amsterdam], ISR Software (ARMV8EL_LINUX_IOSD-UNIVERSALK9-M), Version 17.3.5, RELEASE SOFTWARE (fc2)
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Compiled Wed 09-Feb-22 09:57 by mcpre

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All TCP AO KDF Tests Pass

cisco C1121X-8P (1RU) processor with 1987776K/6147K bytes of memory.

Processor board ID FGL2441LF2E

Router operating mode: Autonomous

1 Virtual Ethernet interface

10 Gigabit Ethernet interfaces

32768K bytes of non-volatile configuration memory.

8388608K bytes of physical memory.

6336511K bytes of flash memory at bootflash:.

WARNING: Command has been added to the configuration using a type 0 password. However, type 0 passwords will soon be deprecated. Migrate to a supported password type

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Press RETURN to get started!

R2>

R2>en

Password:

R2#sh plat

Chassis type: C1121X-8P

Slot	Type	State	Insert time (ago)
0	C1121X-8P	ok	00:03:10
0/0	C1121X-2x1GE	ok	00:02:01
0/1	C1121X-ES-8	ok	00:02:01
R0	C1121X-8P	ok, active	00:03:10
F0	C1121X-8P	ok, active	00:03:10
P0	PWR-12V	ok	00:02:38

Slot	CPLD Version	Firmware Version
0	18103101	17.5(1r)
R0	18103101	17.5(1r)

F0 18103101 17.5(1r)

Initiated SWIM from Catalyst center to 17.12.03a version.

R2#

R2#

R2#dir | i bin

17 -rw- 387448832 Jul 11 2024 16:16:12 +00:00 c1100-universalk9.17.12.03a.SPA.bin

15 -rw- 697507636 Jul 11 2024 16:07:01 +00:00 c1100-universalk9.17.12.01a.SPA.bin

29 -rw- 604409868 Jul 11 2024 15:33:04 +00:00 c1100-universalk9.17.03.05.SPA.bin

21 -rw- 685683088 Sep 25 2023 08:55:21 +00:00 c1100-universalk9.17.09.03a.SPA.bin

R2#dir | i bin

17 -rw- 388956160 Jul 11 2024 16:16:14 +00:00 c1100-universalk9.17.12.03a.SPA.bin

15 -rw- 697507636 Jul 11 2024 16:07:01 +00:00 c1100-universalk9.17.12.01a.SPA.bin

29 -rw- 604409868 Jul 11 2024 15:33:04 +00:00 c1100-universalk9.17.03.05.SPA.bin

21 -rw- 685683088 Sep 25 2023 08:55:21 +00:00 c1100-universalk9.17.09.03a.SPA.bin

R2#

*Jul 11 16:17:37.494: %SEC_LOGIN-5-LOGIN_SUCCESS: Login Success [user: cisco] [Source: 10.104.xxx.xxx] [localport: 22] at 16:17:37 UTC Thu Jul 11 2024

R2#

*Jul 11 16:17:39.199: %SYS-6-LOGOUT: User cisco has exited tty session 1(10.104.xxx.xxx)

R2#

*Jul 11 16:17:40.423: %SEC_LOGIN-5-LOGIN_SUCCESS: Login Success [user: cisco] [Source: 10.104.xxx.xxx] [localport: 22] at 16:17:40 UTC Thu Jul 11 2024

R2#

*Jul 11 16:18:18.113: %SYS-6-LOGOUT: User cisco has exited tty session 1(10.104.xxx.xxx)

R2#

*Jul 11 16:18:19.122: %SEC_LOGIN-5-LOGIN_SUCCESS: Login Success [user: cisco] [Source: 10.104.xxx.xxx] [localport: 22] at 16:18:19 UTC Thu Jul 11 2024

R2#

*Jul 11 16:18:31.107: %SYS-6-LOGOUT: User cisco has exited tty session 1(10.104.xxx.xxx)

R2#

*Jul 11 16:18:32.076: %SEC_LOGIN-5-LOGIN_SUCCESS: Login Success [user: cisco] [Source: 10.104.xxx.xxx] [localport: 22] at 16:18:32 UTC Thu Jul 11 2024

R2#

*Jul 11 16:21:17.934: %SYS-6-LOGOUT: User cisco has exited tty session 1(10.104.xxx.xxx)

R2#

R2 con0 is now available

Press RETURN to get started.

```
*Jul 11 16:26:15.289: %SYS-6-TTY_EXPIRE_TIMER: (exec timer expired, tty 0
(0.0.0.0)), user
*Jul 11 16:26:18.281: %SEC_LOGIN-5-LOGIN_SUCCESS: Login Success [user:
cisco] [Source: 10.104.xxx.xxx] [localport: 22] at 16:26:18 UTC Thu Jul 11 2024
*Jul 11 16:26:27.294: %SYS-5-CONFIG_I: Configured from console by cisco on vty1
(10.104.xxx.xxx)
*Jul 11 16:26:32.600: %SYS-6-PRIVCFG_ENCRYPT_SUCCESS: Successfully
encrypted private config file
*Jul 11 16:26:33.147: %SYS-5-CONFIG_I: Configured from console by cisco on vty1
(10.104.xxx.xxx)
*Jul 11 16:26:37.483: %SYS-6-PRIVCFG_ENCRYPT_SUCCESS: Successfully
encrypted private config file
*Jul 11 16:26:39.424: %SYS-5-RELOAD: Reload requested by cisco on vty1
(10.104.xxx.xxx). Reload Reason: Reload Command.Rom image verified correctly
```

System Bootstrap, Version 17.5(1r), RELEASE SOFTWARE
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Current image running: Boot ROM1

Last reset cause: LocalSoft
C1121X-8P platform with 8388608 Kbytes of main memory

.....

[illegible]

[illegible]

##

Package header rev 3 structure detected
IsoSize = 645599232
Performing Integrity Check ...
Performing Signature Verification ...
RSA Signed RELEASE Image Signature Verification Successful
Image validated

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Cisco Systems, Inc.
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San Jose, California 95134-1706

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All TCP AO KDF Tests Pass
cisco C1121X-8P (1RU) processor with 1987776K/6147K bytes of memory.
Processor board ID FGL2441LF2E
Router operating mode: Autonomous
1 Virtual Ethernet interface
10 Gigabit Ethernet interfaces
32768K bytes of non-volatile configuration memory.
8388608K bytes of physical memory.
6336511K bytes of flash memory at bootflash:.

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Press RETURN to get started!

R2>

R2>en

Password:

R2#sh ver

Cisco IOS XE Software, Version 17.12.03a

Cisco IOS Software [Dublin], ISR Software (ARMV8EL_LINUX_IOSD-UNIVERSALK9-M), Version 17.12.3a, RELEASE SOFTWARE (fc1)

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ROM: 17.5(1r)

R2 uptime is 5 minutes
Uptime for this control processor is 6 minutes
System returned to ROM by Reload Command
System image file is "bootflash:c1100-universalk9.17.12.03a.SPA.bin"
Last reload reason: Reload Command

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If you require further assistance please contact us by sending email to export@cisco.com.

Suite License Information for Module:'esg'

Suite	Suite Current	Type	Suite Next reboot
FoundationSuiteK9 securityk9 appxk9	None	Smart License	None

Technology Package License Information:

Technology	Technology-package Current	Type	Technology-package Next reboot
appxk9	appxk9	Smart License	appxk9
uck9	None	Smart License	None
securityk9	securityk9	Smart License	securityk9
ipbase	ipbasek9	Smart License	ipbasek9

The current throughput level is unthrottled

Smart Licensing Status: Smart Licensing Using Policy

cisco C1121X-8P (1RU) processor with 1914630K/6147K bytes of memory.
Processor board ID FGL2441LF2E
Router operating mode: Autonomous
4 Virtual Ethernet interfaces
10 Gigabit Ethernet interfaces
32768K bytes of non-volatile configuration memory.
8388608K bytes of physical memory.
6336511K bytes of flash memory at bootflash:.

Configuration register is 0x2102

R2# sh plat
Chassis type: C1121X-8P

Slot	Type	State	Insert time (ago)
0	C1121X-8P	ok	00:05:56
0/0	C1121X-2x1GE	ok	00:04:49
0/1	C1121X-ES-8	ok	00:04:49
R0	C1121X-8P	ok, active	00:05:56
F0	C1121X-8P	ok, active	00:05:56
P0	PWR-12V	ok	00:05:11

Slot	CPLD Version	Firmware Version
0	18103101	17.5(1r)
R0	18103101	17.5(1r)
F0	18103101	17.5(1r)